



SANYO Semiconductors

DATA SHEET

LV49152V — Bi-CMOS LSI Class-D Audio Power Amplifier BTL 15W × 2ch

Overview

The LV49152V is a 15W per channel stereo digital power amplifier that takes analog inputs. The LV49152V uses unique SANYO-developed feedback technology to achieve excellent audio quality despite being a class D amplifier and can be used to implement high quality flat display panel (FDP) based systems.

Features

- BTL output, class D amplifier system
- Unique SANYO-developed feedback technology achieves superb audio quality
- High-efficiency class D amplifier
- Soft muting function reduces impulse noise at power on/off
- Full complement of built-in protection circuits : over current protection, thermal protection, and low power supply voltage protection circuits
- Built in Power limiter

Functions

- Power : 15W × 2ch output (VD = 15V, R_L = 8Ω, fin = 1kHz, AES17, THD + N = 10%)
- Efficiency : 93% (VD = 15V, R_L = 8Ω, fin = 1kHz, P_O = 15W)
- THD + N : 0.08% (VD = 15V, R_L = 8Ω, fin = 1kHz, P_O = 1W, Filter : AES17)
- Noise : 90μVrms (Filter : A-weight)
- Package SSOP44J (275mil)

■ Any and all SANYO Semiconductor Co.,Ltd. products described or contained herein are, with regard to "standard application", intended for the use as general electronics equipment (home appliances, AV equipment, communication device, office equipment, industrial equipment etc.). The products mentioned herein shall not be intended for use for any "special application" (medical equipment whose purpose is to sustain life, aerospace instrument, nuclear control device, burning appliances, transportation machine, traffic signal system, safety equipment etc.) that shall require extremely high level of reliability and can directly threaten human lives in case of failure or malfunction of the product or may cause harm to human bodies, nor shall they grant any guarantee thereof. If you should intend to use our products for applications outside the standard applications of our customer who is considering such use and/or outside the scope of our intended standard applications, please consult with us prior to the intended use. If there is no consultation or inquiry before the intended use, our customer shall be solely responsible for the use.

■ Specifications of any and all SANYO Semiconductor Co.,Ltd. products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

LV49152V

Specifications

Absolute Maximum Ratings at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	VD	Supply voltage	20	V
Allowable power dissipation	Pd max	Our PCB, Soldered *	5.05	W
Package thermal resistance	θ_{jc}	Our PCB, Soldered *	2.1	$^\circ\text{C}/\text{W}$
		Our PCB, Not soldered *	3.6	$^\circ\text{C}/\text{W}$
Maximum junction temperature	Tj max		150	$^\circ\text{C}$
Operating temperature	Topr		-25 to +75	$^\circ\text{C}$
Storage temperature	Tstg		-50 to +150	$^\circ\text{C}$

* : Mounted on a specified board 11.0mm x 9.5mm x 1.5mm, glass epoxy (two-layer)

Recommended Operating Range at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Supply voltage range	VD	Supply voltage	9	15	18	V
Load impedance range	R _L	Speaker load	4	8		Ω

Electrical Characteristics at $T_a = 25^\circ\text{C}$, VD = 15V, R_L = 8 Ω , L = 33 μH (TOKO : A7502BY-330M), C = 0.1 μF , C_L = 0.47 μF

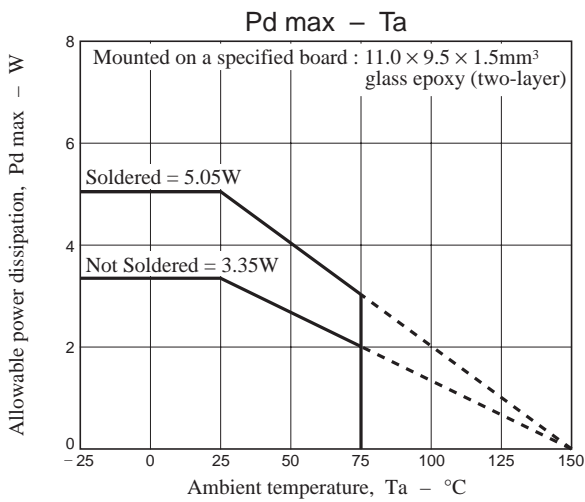
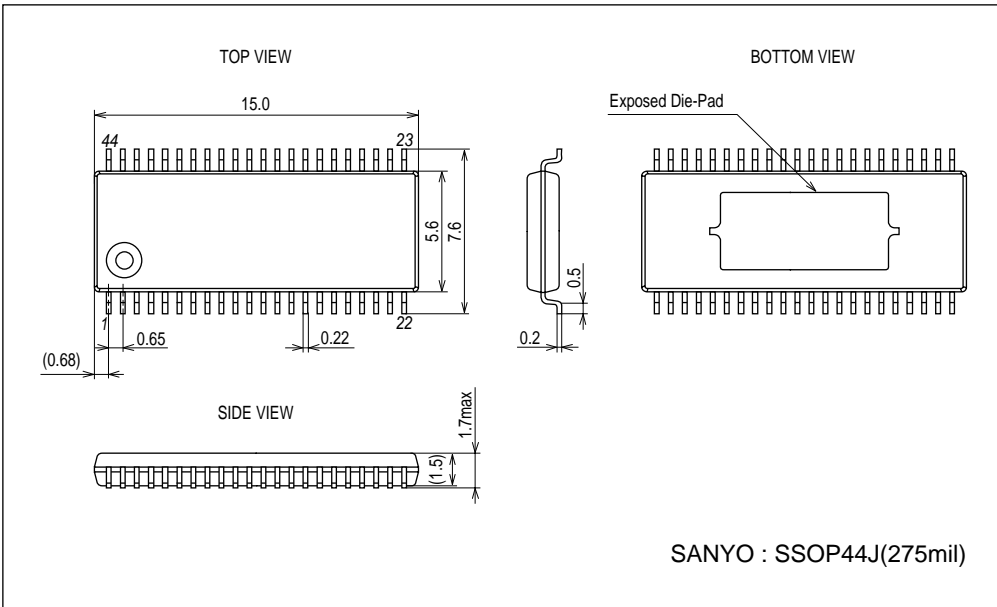
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Standby current	I _{st}	$\overline{\text{STBY}} = \text{L}, \overline{\text{MUTE}} = \text{L}$		1	10	μA
Mute current	I _{mute}	$\overline{\text{STBY}} = \text{H}, \overline{\text{MUTE}} = \text{L}$	14	20	26	mA
Quiescent current	I _{CCO}	$\overline{\text{STBY}} = \text{H}, \overline{\text{MUTE}} = \text{H}$	35	45	55	mA
Voltage gain	VG	f _{in} = 1kHz, V _O = 0dBm	28	30	32	dB
Offset voltage	V _{offset}	R _g = 0	-150		150	mV
Total harmonic distortion	THD+N	P _O = 1W, f _{in} = 1kHz, AES17		0.08	0.4	%
Output power	P _{O@10%}	THD+N = 10%, AES17	13	15		W
Channel separation	CHsep.	R _g = 0, V _O = 0dBm, DIN AUDIO	55	70		dB
Ripple rejection ratio	SVRR	f _r = 100Hz, V _r = 0dBm, R _g = 0, DIN AUDIO	50	60		dB
Noise	V _{NO}	R _g = 0, A-weight		90	300	μV_{rms}
High-level input voltage	V _{IH}	$\overline{\text{STBY}}$ and $\overline{\text{MUTE}}$ pin	3		VD	V
Low-level input voltage	V _{IL}	$\overline{\text{STBY}}$ and $\overline{\text{MUTE}}$ pin	0		1	V
Under voltage protection UPPER	UV_UPPER	VD voltage measure		8.0		V
Under voltage protection LOWER	UV_LOWER	VD voltage measure		7.0		V

Note : The values of these characteristics were measured in the SANYO test environment. The actual values in an end system will vary depending on the printed circuit board pattern, the external components actually used, and other factors.

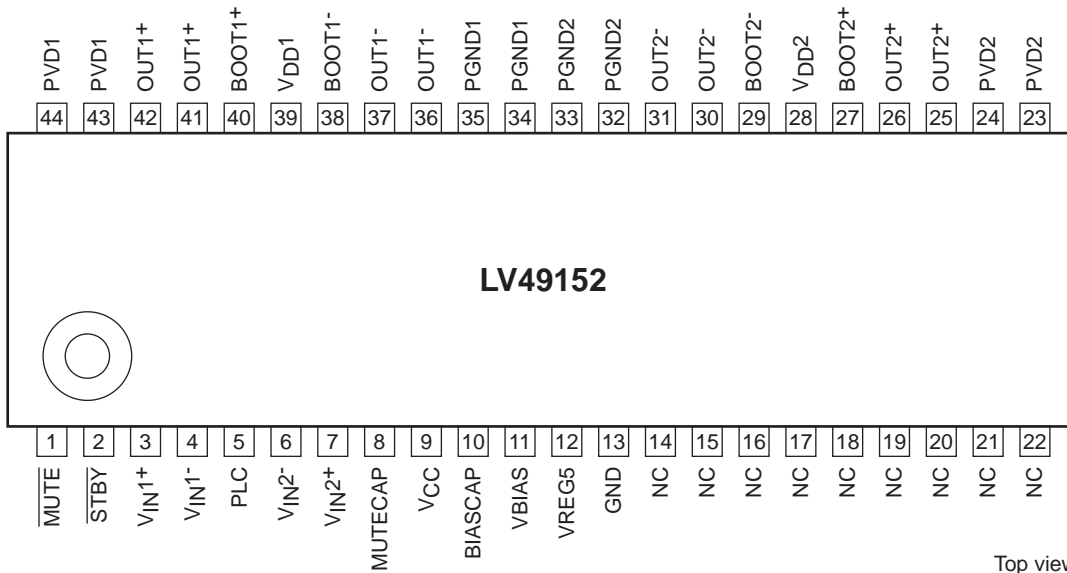
LV49152V

Package Dimensions

unit : mm (typ)
3285

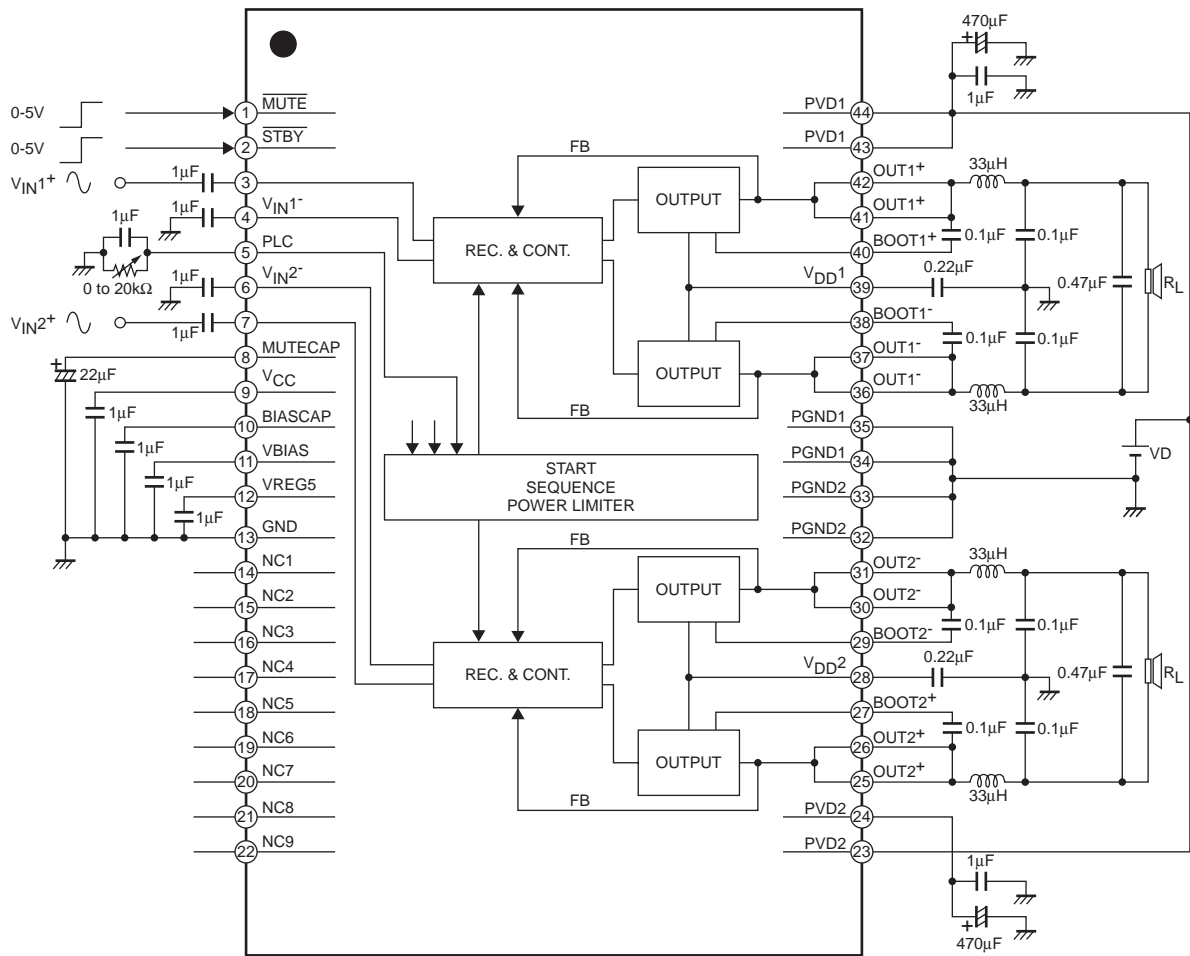


Pin Assignment



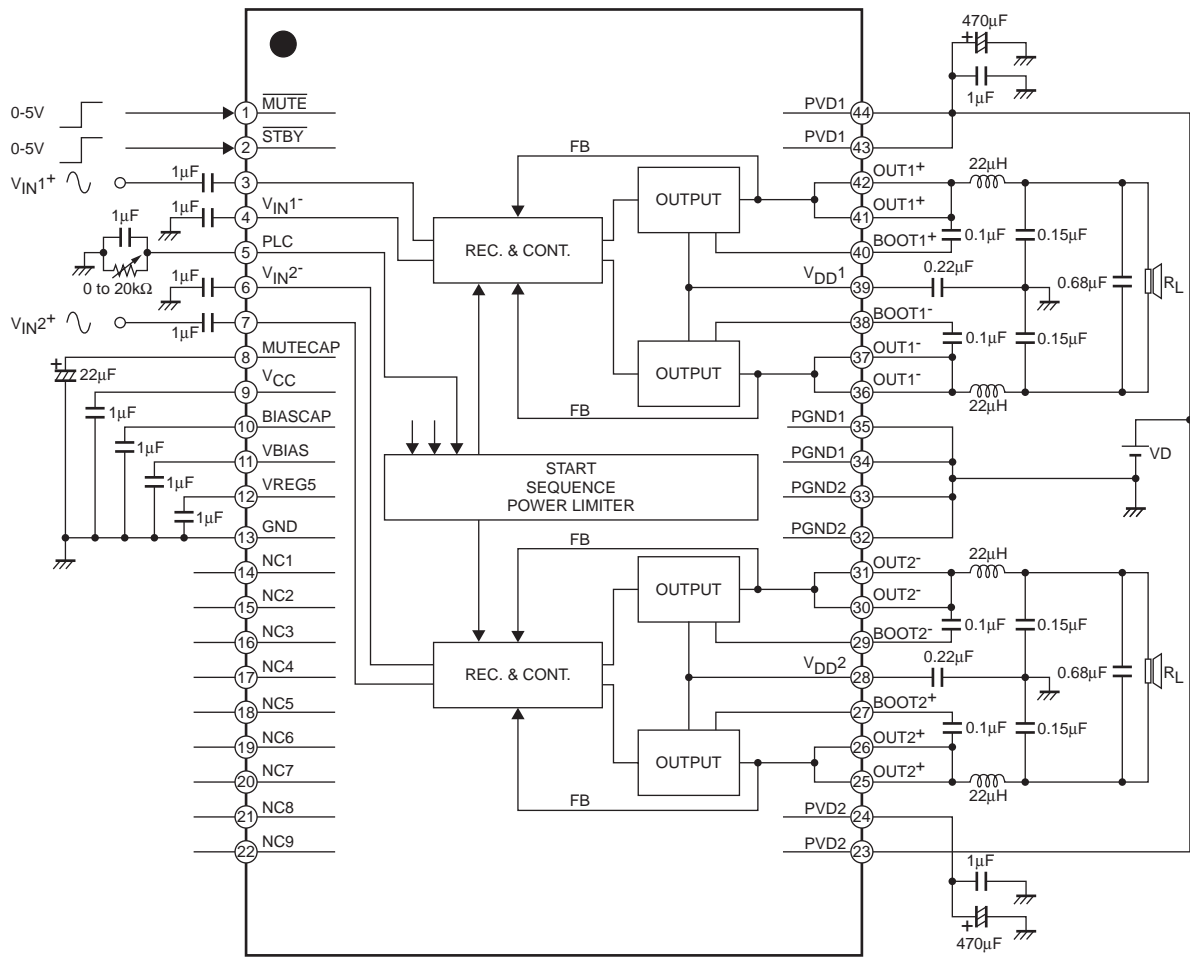
LV49152V

Block Diagram and Application Circuit Example 1 ($R_L = 8\Omega$)



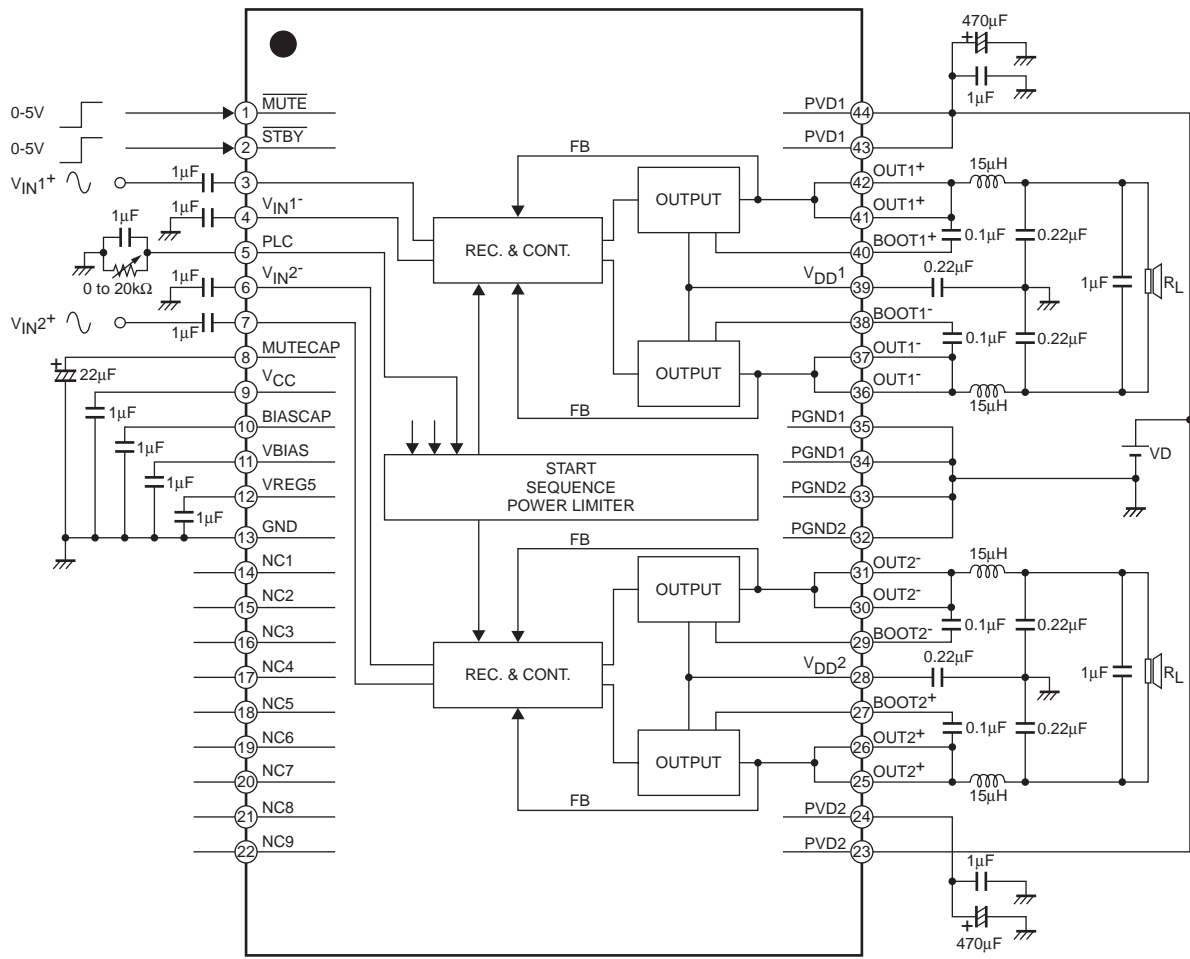
LV49152V

Application Circuit Example 2 ($R_L = 6\Omega$)



LV49152V

Application Circuit Example 3 ($R_L = 4\Omega$)



LV49152V

Pin Equivalent Circuit

Pin No.	Pin name	I/O	Description	Equivalent Circuit
1	MUTE	I	Mute control pin	
2	STBY	I	Standby control pin	
3	V _{IN1+}	I	Input pin, CH1 plus	
4	V _{IN1-}	I	Input pin, CH1 minus	
5	PLC	I	Power level control pin	

Continued on next page.

LV49152V

Continued from preceding page.

Pin No.	Pin name	I/O	Description	Equivalent Circuit
6	V_{IN2^-}	I	Input pin, CH2 minus	
7	V_{IN2^+}	I	Input pin, CH2 plus	
8	MUTECAP	O	Muteing sysytem capcitor connection	
9	V_{CC}	O	Internal power supply decupling capacitor connection	
10	BIASCAP	O	Internal regulator decupling capacitor connection	

Continued on next page.

LV49152V

Continued from preceding page.

Pin No.	Pin name	I/O	Description	Equivalent Circuit
11	VBIAS	O	Internal regulator decoupling capacitor connection	
12	VREG5	O	Internal regulator decoupling capacitor connection	
13	GND		Analog Ground	
14	NC		Non connection	
15	NC		Non connection	
16	NC		Non connection	
17	NC		Non connection	
18	NC		Non connection	
19	NC		Non connection	
20	NC		Non connection	
21	NC		Non connection	
22	NC		Non connection	
23	PVD2		CH2 power supply	
24	PVD2		CH2 power supply	
25	OUT2 ⁺	O	Output pin, CH2 plus	
26	OUT2 ⁺	O	Output pin, CH2 plus	

Continued on next page.

LV49152V

Continued from preceding page.

Pin No.	Pin name	I/O	Description	Equivalent Circuit
27	BOOT2 ⁺	I/O	Boot strap pin, CH2 plus	
28	V _{DD2}	O	CH2 internal regulator decoupling capacitor connection	
29	BOOT2 ⁻	I/O	Boot strap pin, CH2 minus	
30	OUT2 ⁻	O	Output pin, CH2 minus	
31	OUT2 ⁻	O	Output pin, CH2 minus	
32	PGND2		CH2 Power Ground	
33	PGND2		CH2 Power Ground	
34	PGND1		CH1 Power Ground	
35	PGND1		CH1 Power Ground	
36	OUT1 ⁻	O	Output pin, CH1 minus	
37	OUT1 ⁻	O	Output pin, CH1 minus	
38	BOOT1 ⁻	I/O	Boot strap pin, CH1 minus	
39	V _{DD1}	O	CH1 internal regulator decoupling capacitor connection	
40	BOOT1 ⁺	I/O	Boot strap pin, CH1 plus	

Continued on next page.

LV49152V

Continued from preceding page.

Pin No.	Pin name	I/O	Description	Equivalent Circuit
41	OUT1+	O	Output pin, CH1 plus	
42	OUT1+	O	Output pin, CH1 plus	
43	PVD1		CH1 power supply	
44	PVD1		CH1 power supply	

Operation Mode Summary

STBY mode ($\overline{\text{STBY}} = \text{L}$ and $\overline{\text{MUTE}} = \text{L}$)

Each bias becomes off state when the regulator in IC has been turned off.

The most of circuits becomes off state.

The supply current : $1\mu\text{A}$ (typical).

MUTE mode ($\overline{\text{STBY}} = \text{H}$ and $\overline{\text{MUTE}} = \text{L}$)

Each bias becomes on state when the regulator in IC has been turned on.

When more than half of the circuits are active, the amplifier in the output stages become off.

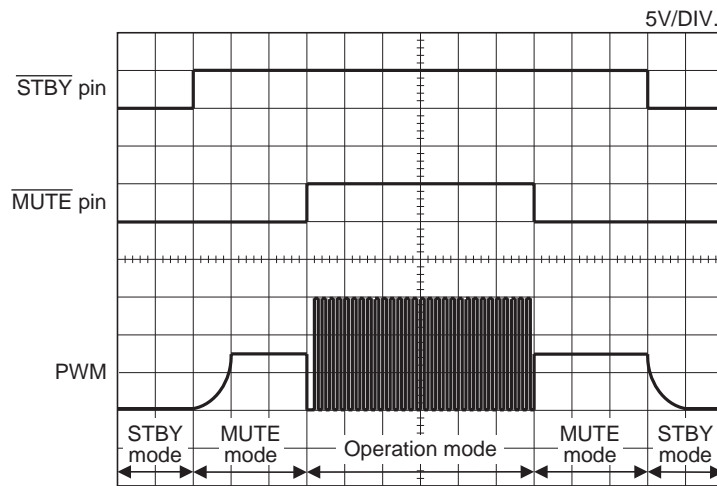
The supply current : 20mA (typical).

Operation mode ($\overline{\text{STBY}} = \text{H}$ and $\overline{\text{MUTE}} = \text{H}$)

The LV49152V operates as D-class amplifier.

The output signal is synchronized with the input signal.

The supply current : 45mA (typical)

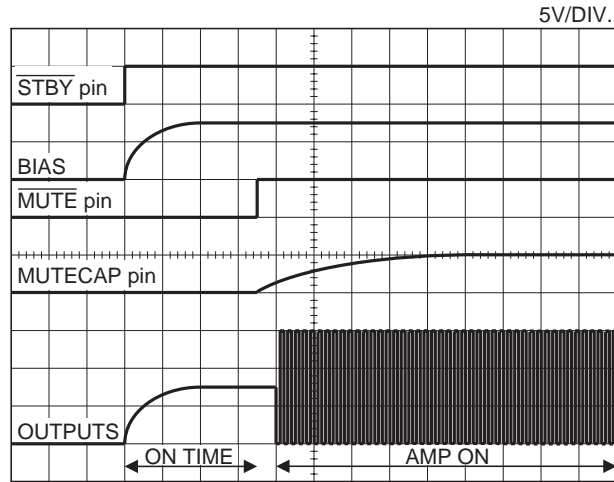


Function image

ON TIME/OFF TIME

ON TIME

Please secure ON TIME of 350msec or more for reducing Pop noise.

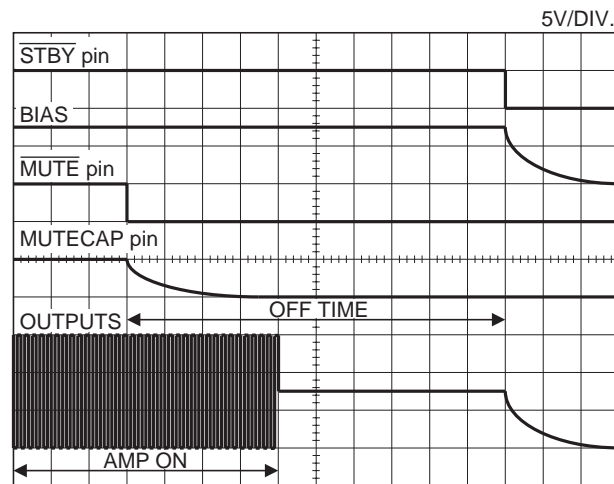


Function image

ON TIME ••• the time until the $\overline{\text{MUTE}}$ pin is set to high level after the $\overline{\text{STBY}}$ pin is set to high level

OFF TIME

Please secure OFF TIME of 1000msec or more for reducing Pop noise.



Function image

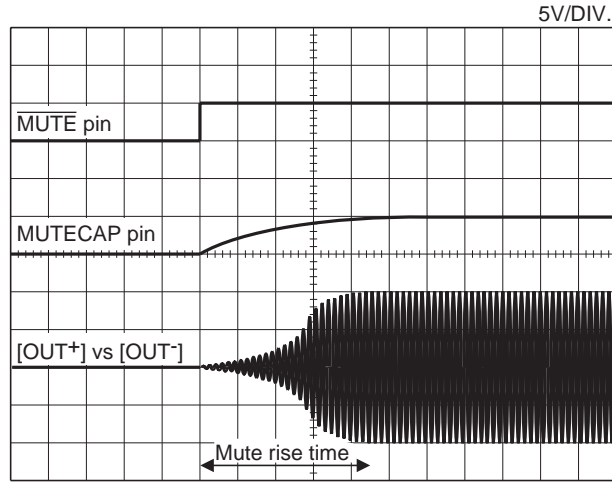
OFF TIME ••• the time until the $\overline{\text{STBY}}$ pin is set to low level after the $\overline{\text{MUTE}}$ pin is set to low level

SOFT MUTE

The soft mute circuit is able to use fade in/fade out function, and can set Rise time and fall time by the time constant of the MUTE CAP capacitor.

FADE IN

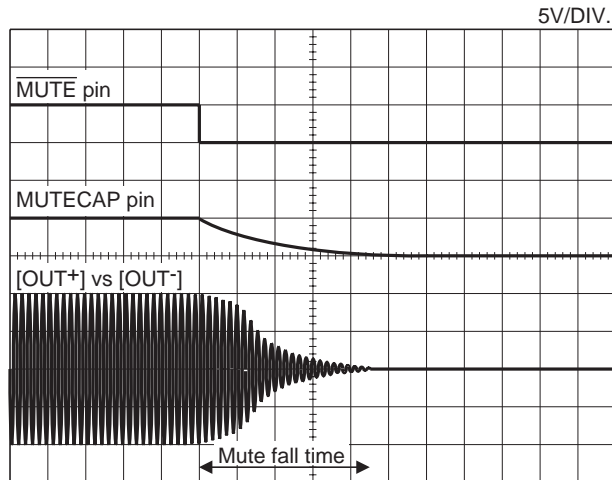
Mute rise time is Applpx.450msec in our recommended external components.



Function image

FADE OUT

Mute fall time is Applpx.450msec in our recommended external components.



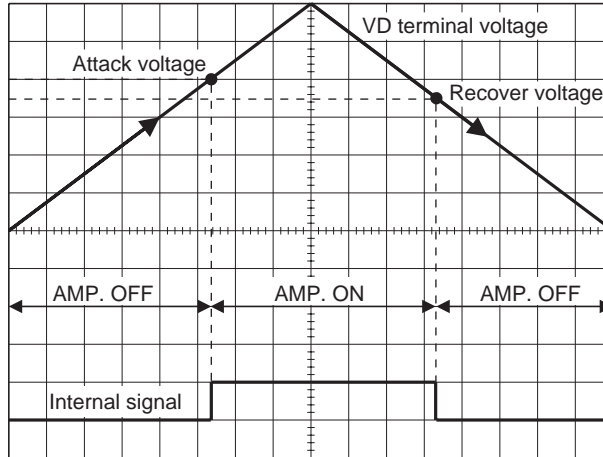
Function image

Power supply lowering protection circuit

Since the instable operation in the low voltage is prevented by using this circuit, after the voltage of the PVD pin is monitored and the voltage below the Attack voltage (PVD = 8V typ.), AMP is turned off.

Also, to prevent the instable operation when the voltage of the PVD pin is decreased by any cause during operations, the Attack voltage (PVD = 7V typ.) is set.

The voltage of Attack and Recover has hysteresis (About 1V) to prevent ON/OFF continuous action of the power supply lowering protection circuit.



Function image

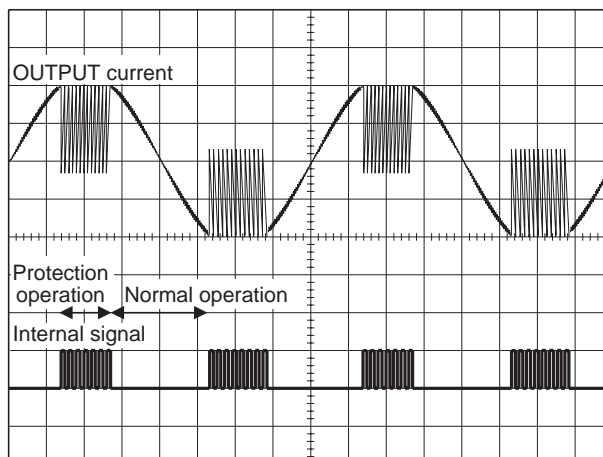
Also, this IC is designed to turn off AMP in the same sequence that the MUTE is on as a pop noise measures when the plug of products are put off.

Over current protection circuit

The over current protection circuit is a protection circuit * to protect the output DMOS from the over current and corresponds to any mode of the power supply, GND and a load short.

The protection operation is performed when the current reaches the detection current value set out in IC and the output DMOS is compulsorily turned off for about 20μsec.

After compulsorily tuning off the output DMOS, when the Amplifier is automatically reset in usual operation and the over current flows continuously, the protection operation is performed again.



Function image

* The over current protection circuit is a function to avoid the abnormal state like the output short-circuit temporarily. Unfortunately, we cannot guarantee that IC is not destroyed.

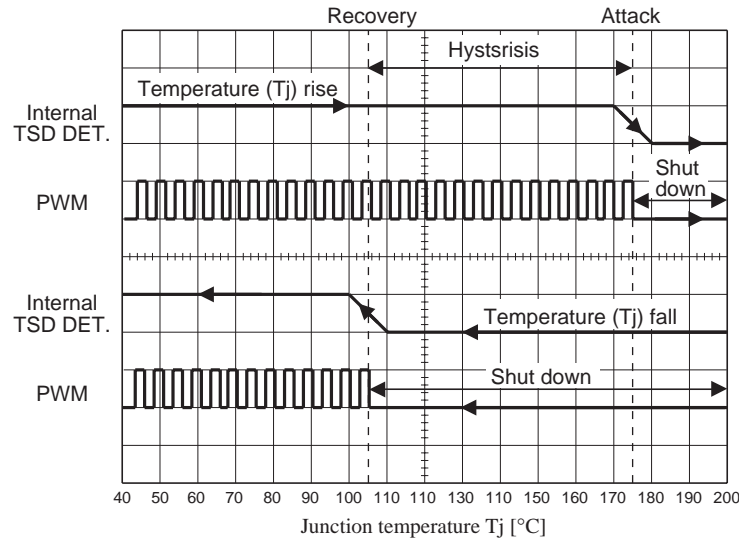
LV49152V

Thermal protection circuit

The LV49152V includes a thermal protection circuit to prevent damage to or destruction of the IC should abnormal internal heat generation occur.

This means that should the IC junction temperature (T_j) rise above about 175°C due to inadequate heat dissipation or other reason, the thermal protection circuit will operate to stop IC operation should the temperature rise further.

If the temperature is reduced by lowering the input level or other means, the thermal protection circuit will recover automatically (about 105°C).



Function image

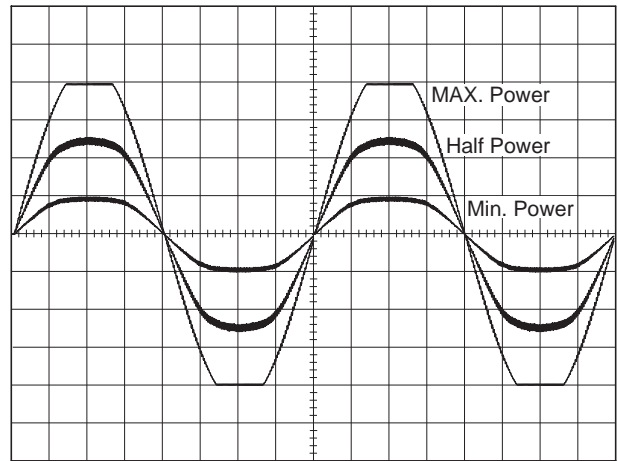
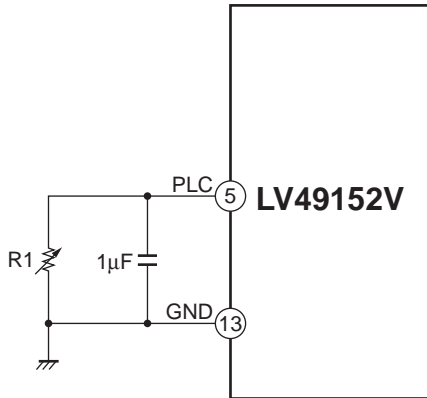
* The thermal protection circuit is a function to avoid the abnormal state temporarily. Unfortunately, we cannot guarantee that IC is not destroyed.

LV49152V

PLC

The PLC (power level control) function is able to control the maximum index modulation by setting a value of external PLC resistance R1 voluntarily, and prevent a PWM signal from becoming the over modulation mode. In addition, this circuit can be used as an output power limit circuit because the PLC function can set the maximum index modulation voluntarily, and variable from 2W to 15W with output power linearly in the state that made the power supply voltage and load resistance fixation. Because the PLC function can set the suitable rated output with the same power supply voltage/speaker regardless of screen size in flat screen televisions by this, set can plan the commonization of the board.

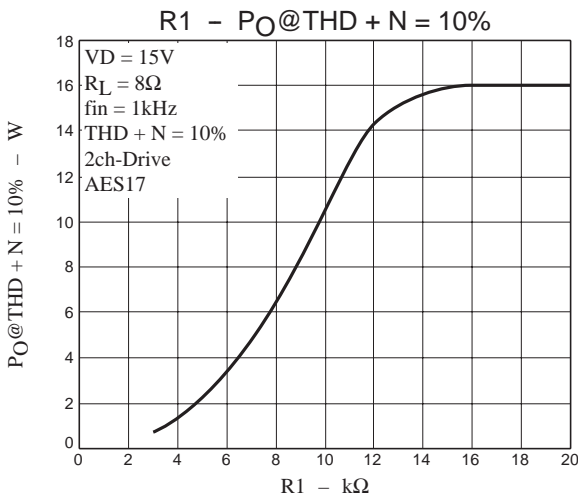
Furthermore, The PLC function can reduce abnormal noise in the hard clip so that output wave pattern becomes the soft clip when it limited output power.



Function image

Measuring condition

VD = 15V, $R_L = 8\Omega$, L = 33 μ H (TOKO : A7502BY-330M), C = 0.1 μ F, $C_L = 0.47\mu$ F, $T_a = 25^\circ\text{C}$

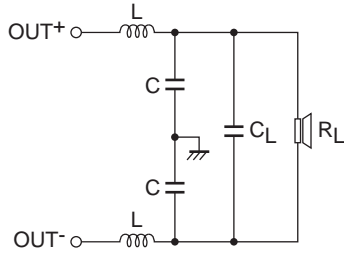


R1 [k Ω]	Po@10% [W]
3.0	0.694
3.6	1.073
4.7	1.982
6.2	3.642
7.5	5.562
8.2	6.855
9.1	8.591
10	10.64
13	15.32
15	15.94
20	16.01

Setting example of the output power limit value

- * When it is used this function as output power limit, please use the high-precision resistance such as the metal film resistor when precision of the electricity value is necessary.
- * The value of external PLC resistance R1 please connects more than 3k Ω .
- * When it is changed a value of external PLC resistance R1, please turn off an amplifier.

Cut-off frequency calculation method and the output LC filter setting



The cut off frequency f_c of the output LC filter is calculated by the following formula.

$$f_c = \frac{1}{2\pi\sqrt{2LC_L}}$$

Also, by setting the cut off frequency f_c , the value of C_L and L is calculated by using the following formula.

$$C_L = \frac{1}{2\sqrt{2} \times \pi R_L f_c}$$

$$L = \frac{\sqrt{2} \times R_L}{4\pi f_c}$$

In general, the value from 20% to 30% of C_L is set to C .

In case of $f_c = 30\text{kHz}$

R_L [Ω]	L [μH]	C_L [μF]	C [μF]	Q
4	15	1	0.22	0.650
6	22	0.68	0.15	0.636
8	33	0.47	0.1	0.704
16	68	0.22	0.047	0.739

Above formula is common calculation method and is a measure of constant setting.

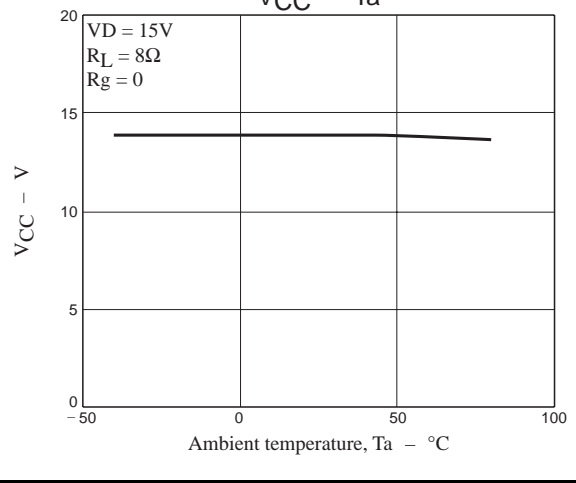
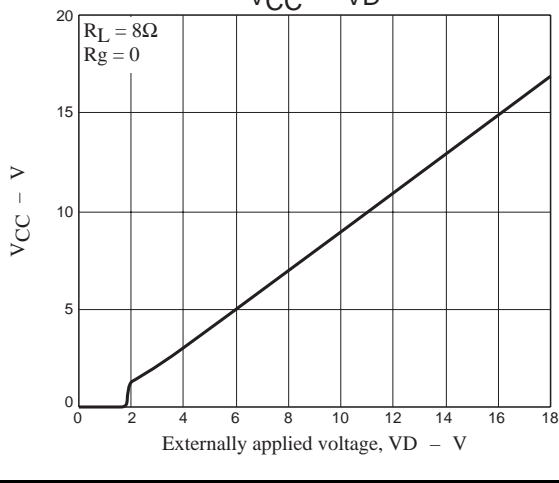
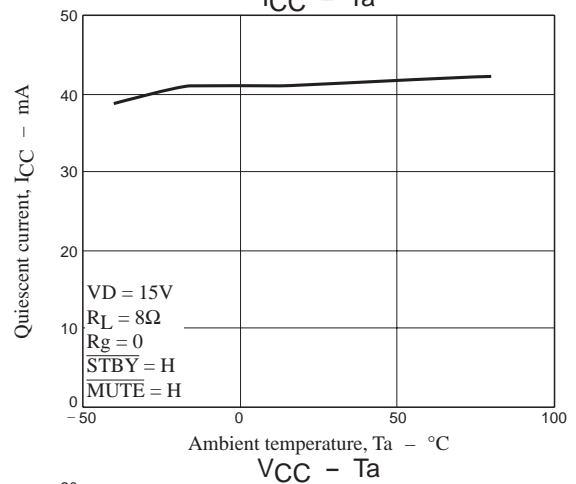
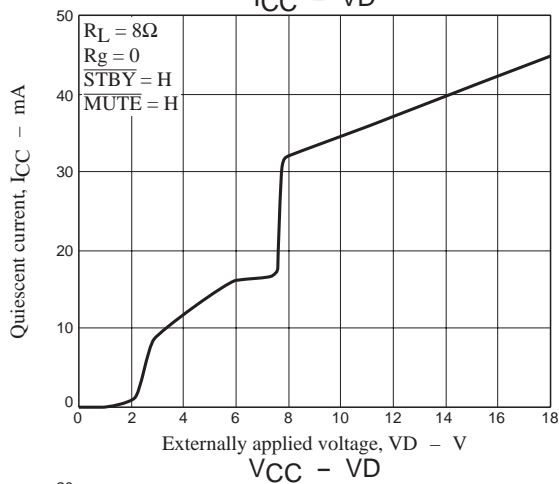
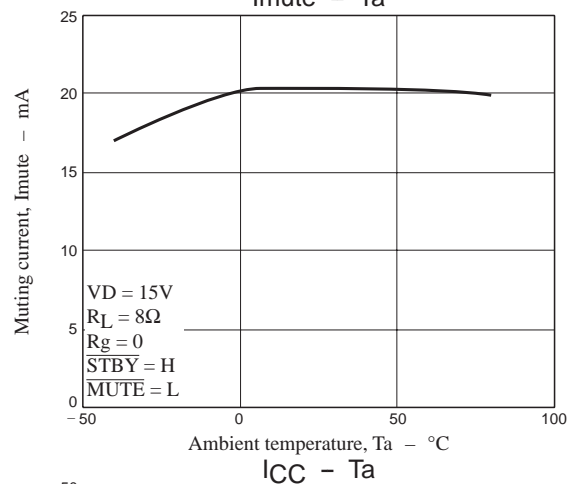
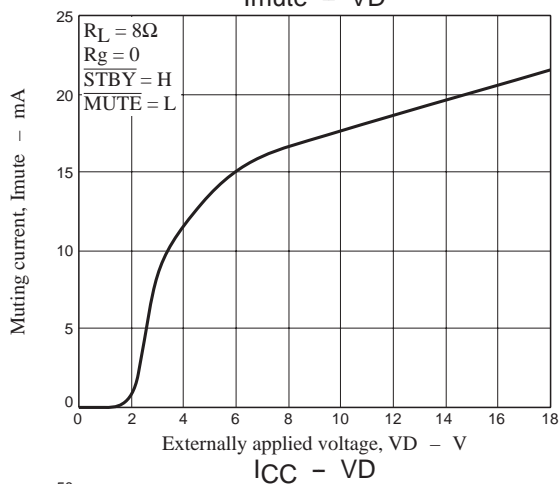
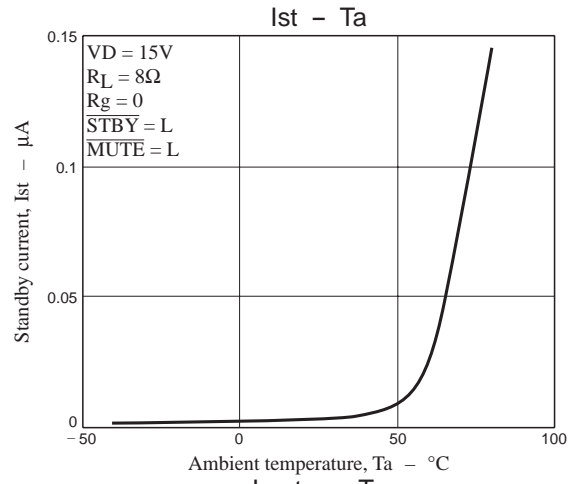
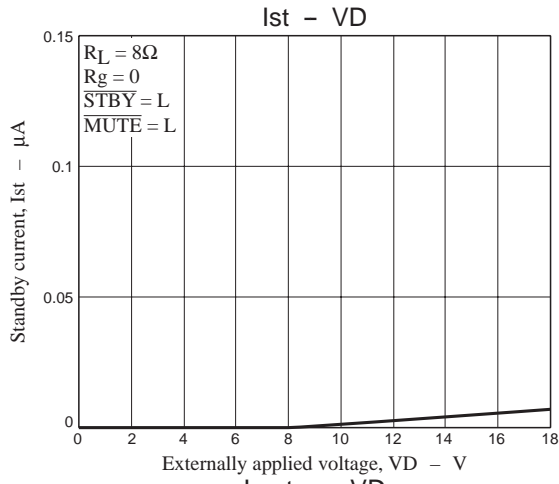
In fact, it is necessary to set with each set that considers the speaker characteristics.

In addition, please set the fixed number to become $Q \leq 1$ in currents in the f_c neighborhood increasing if Q value of the LC filter is big.

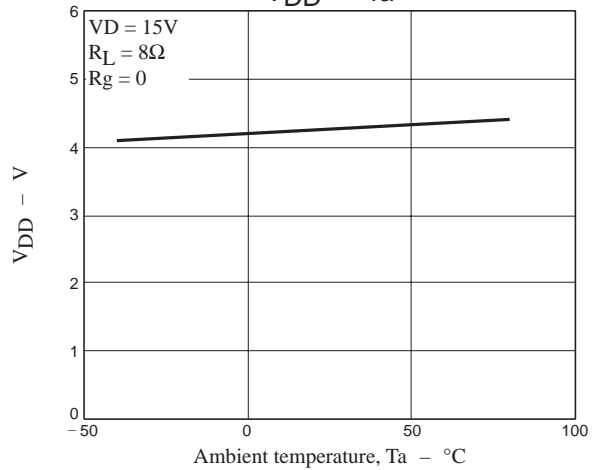
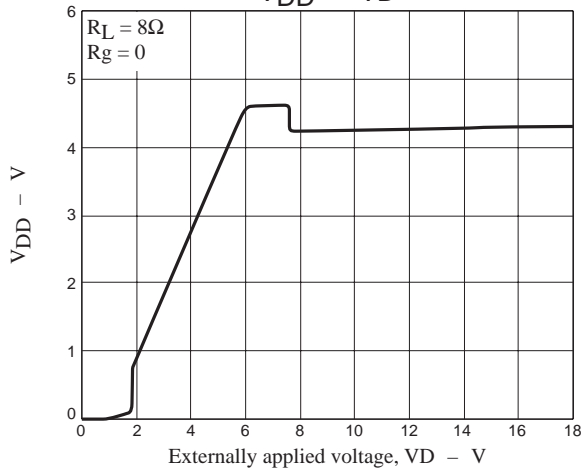
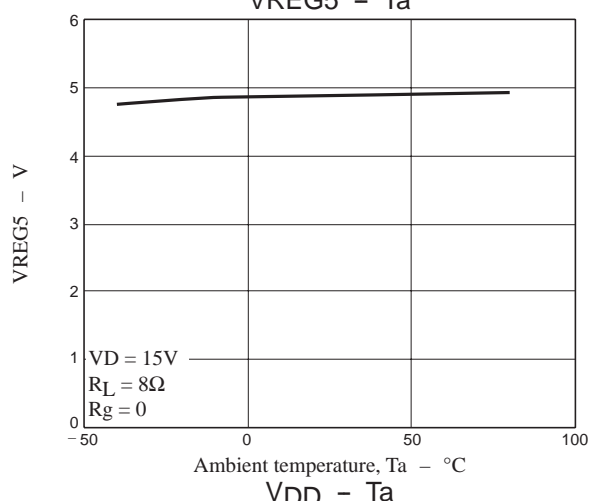
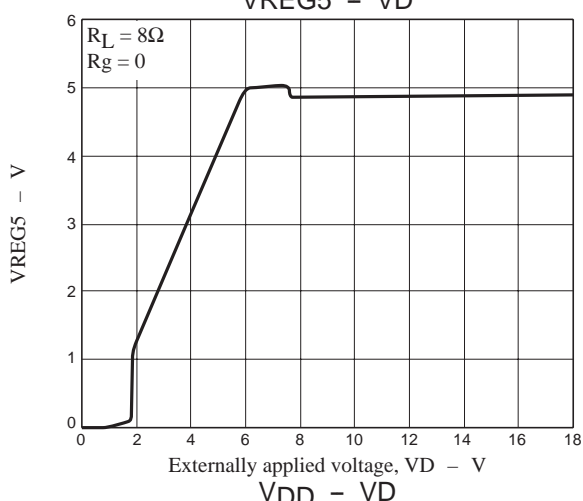
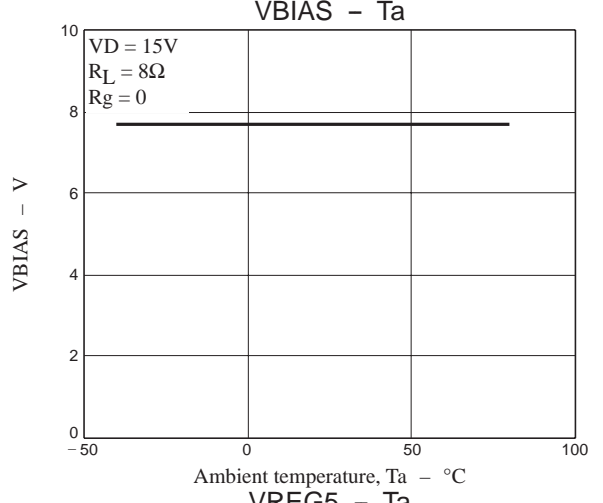
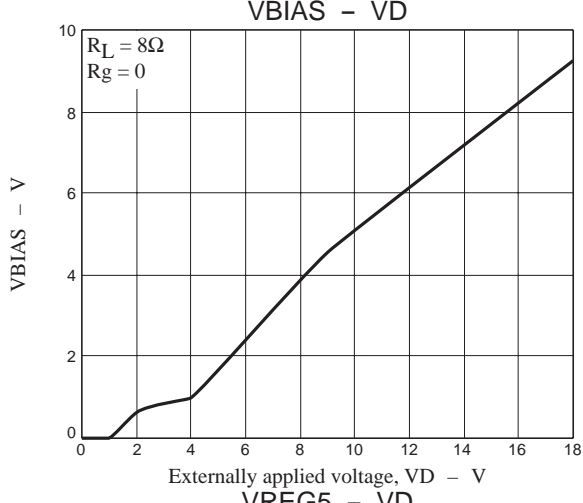
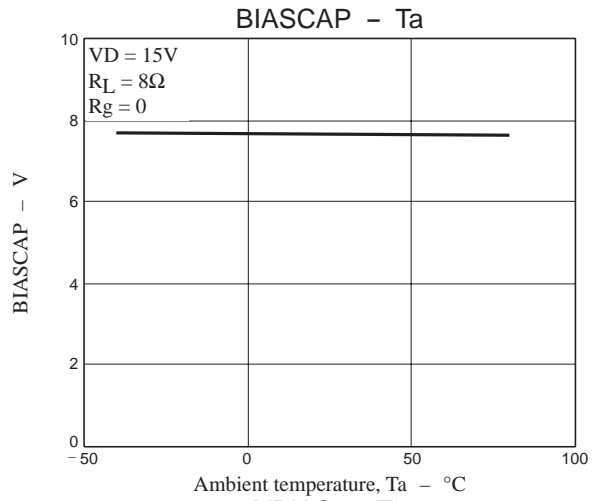
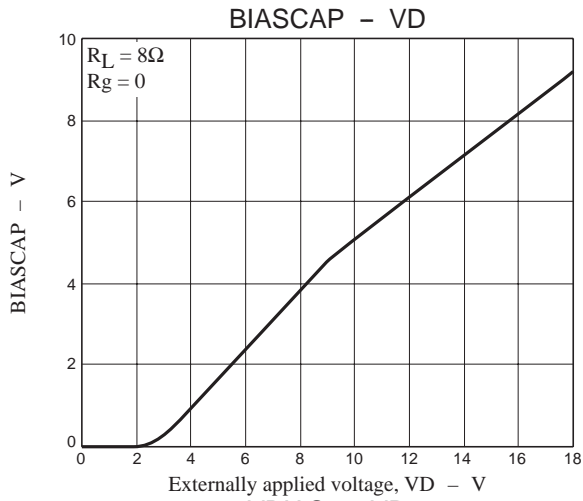
LV49152V

Graph data

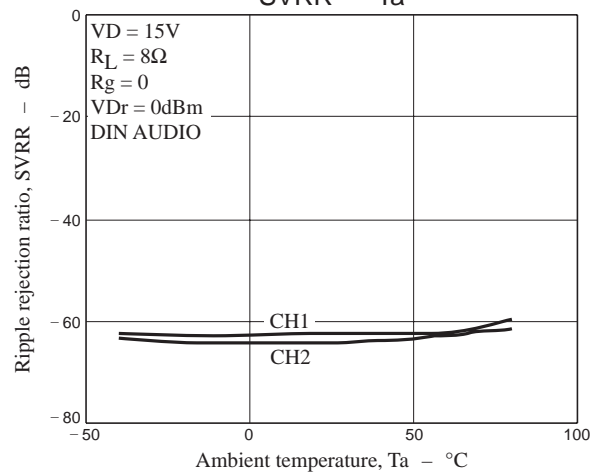
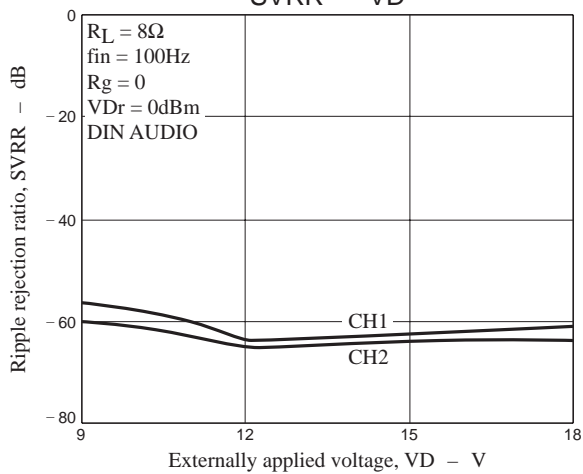
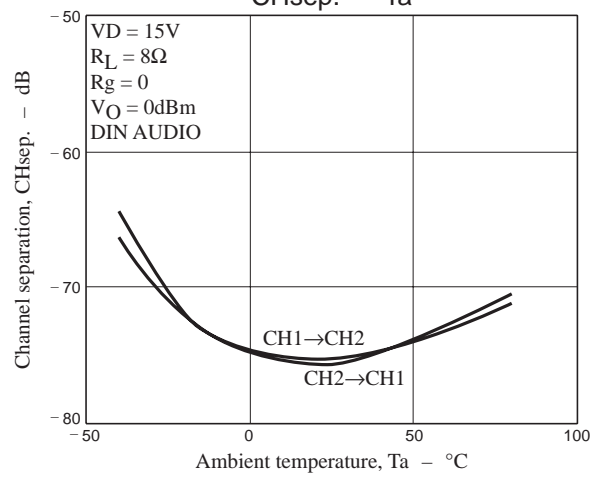
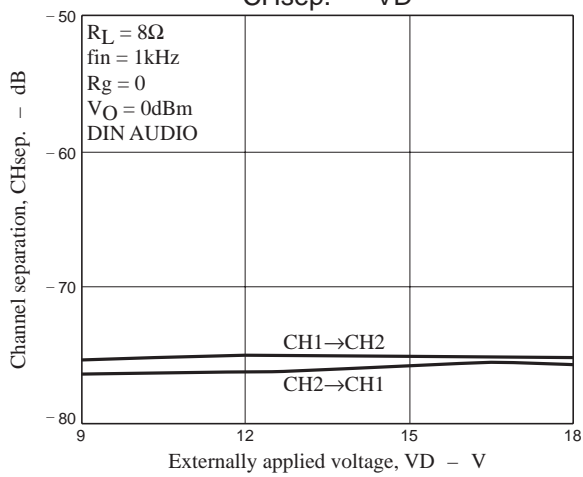
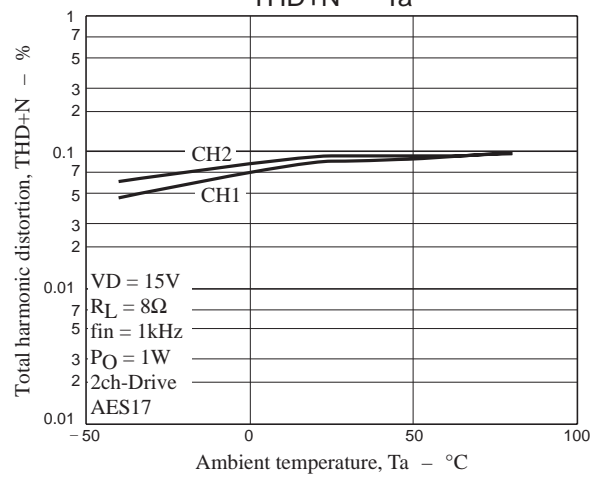
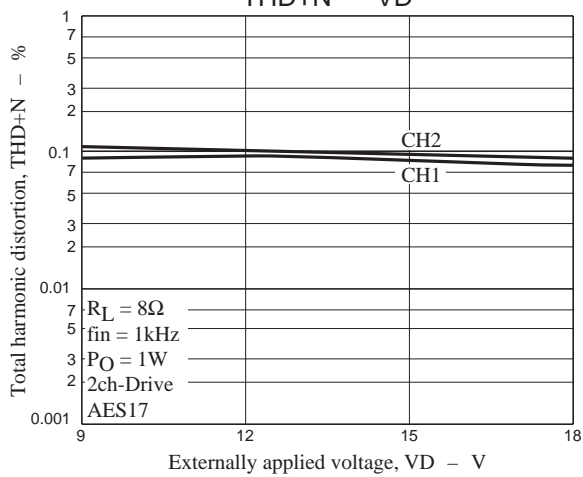
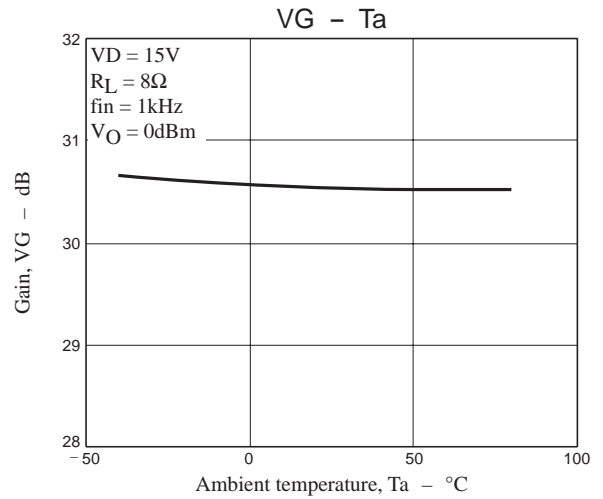
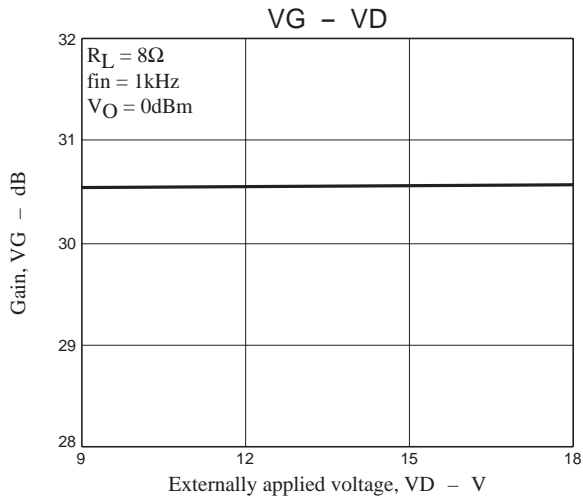
$L = 33\mu\text{H}$ (TOKO : A7502BY-330M), $C = 0.1\mu\text{F}$, $C_L = 0.47\mu\text{F}$



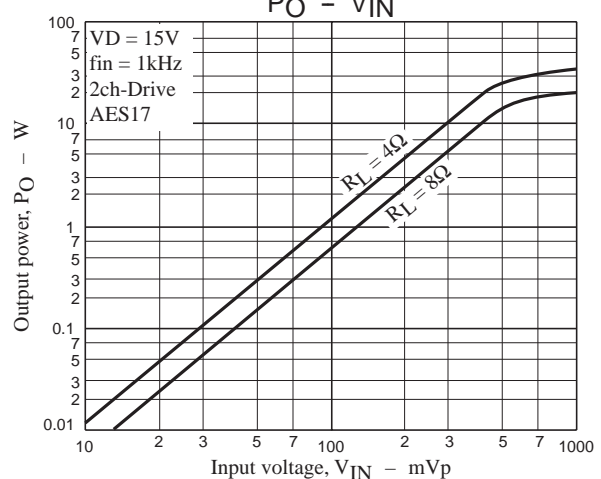
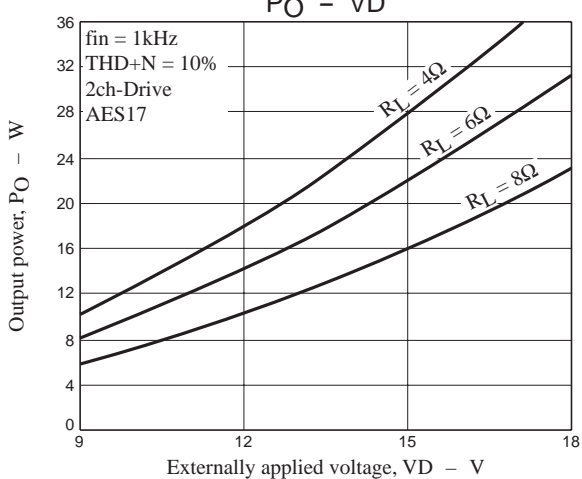
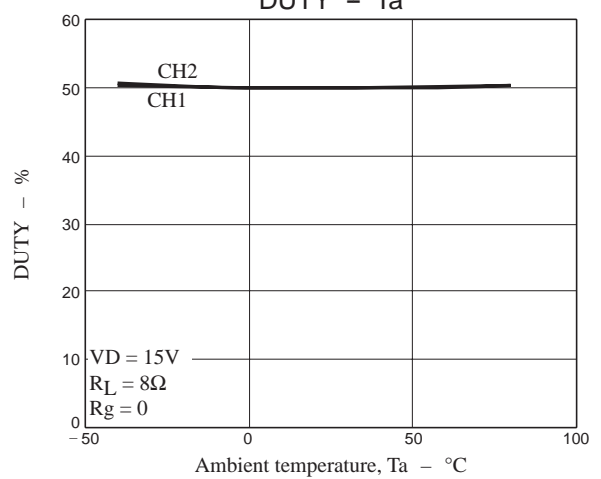
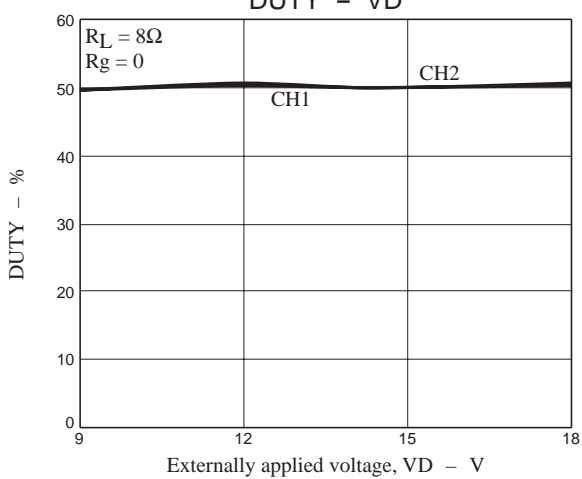
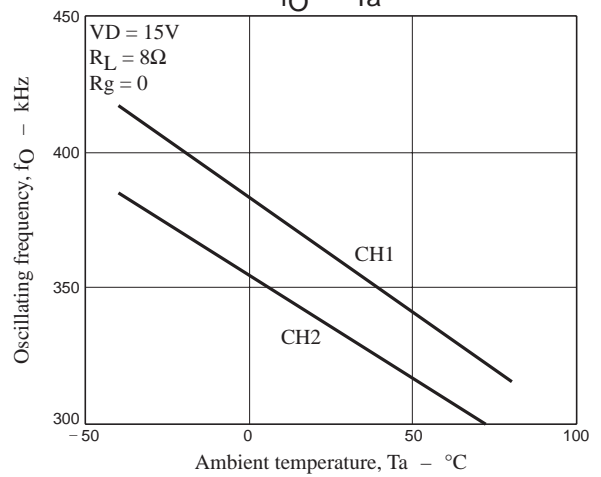
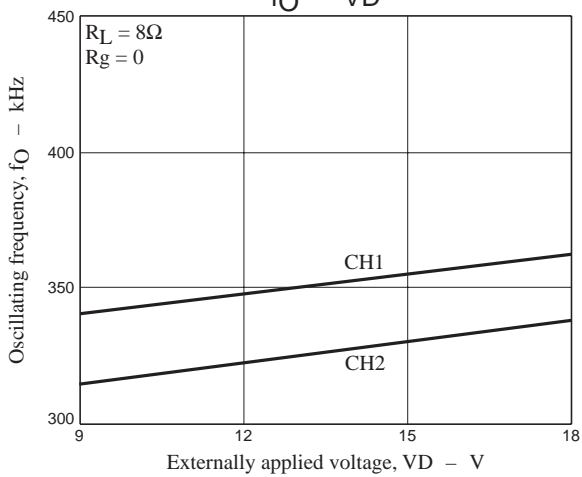
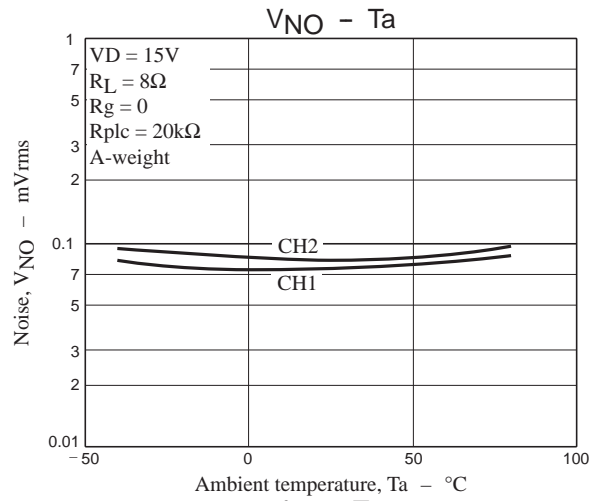
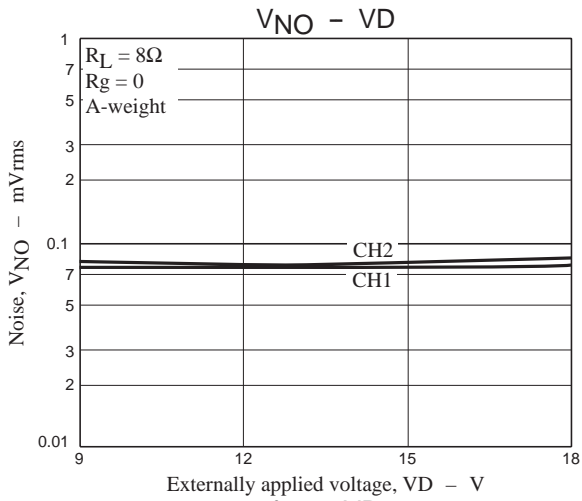
LV49152V



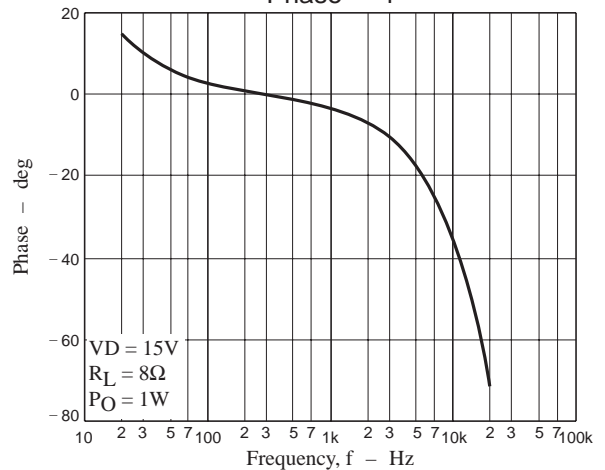
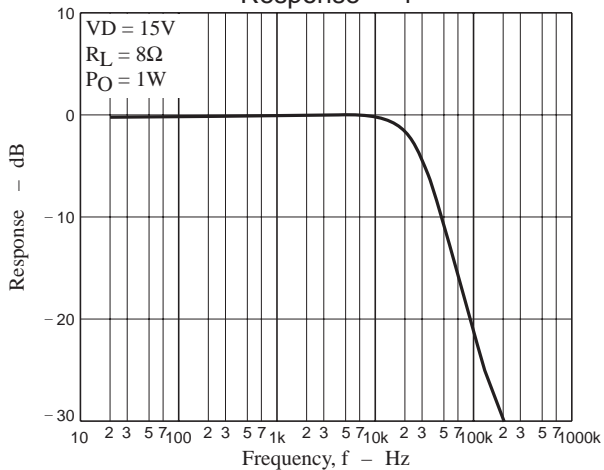
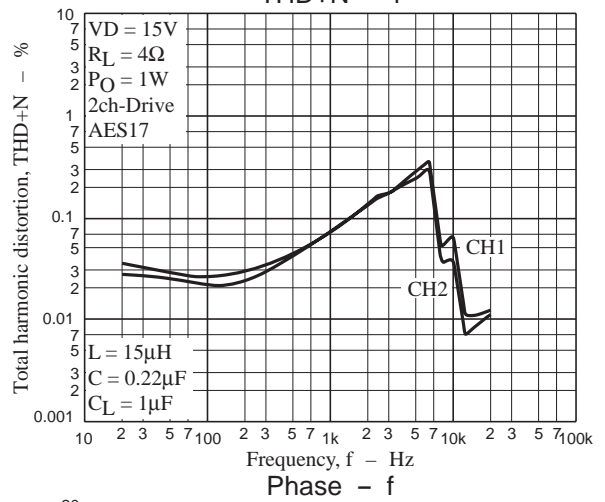
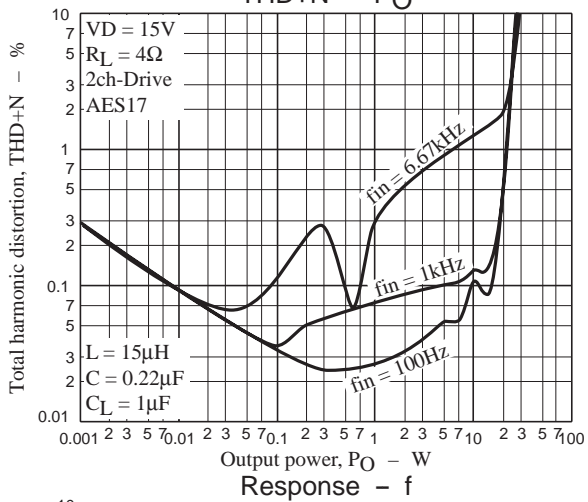
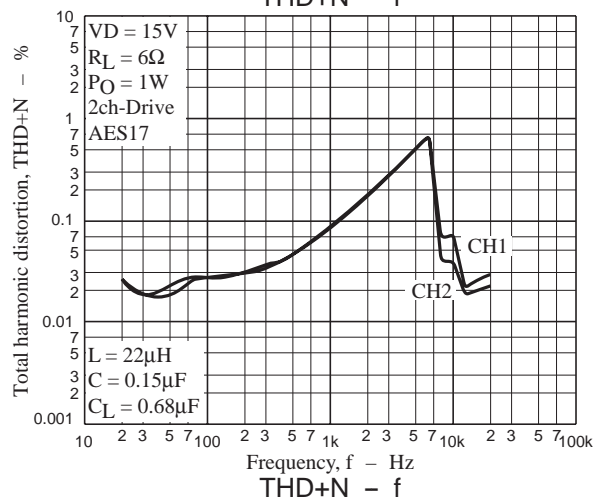
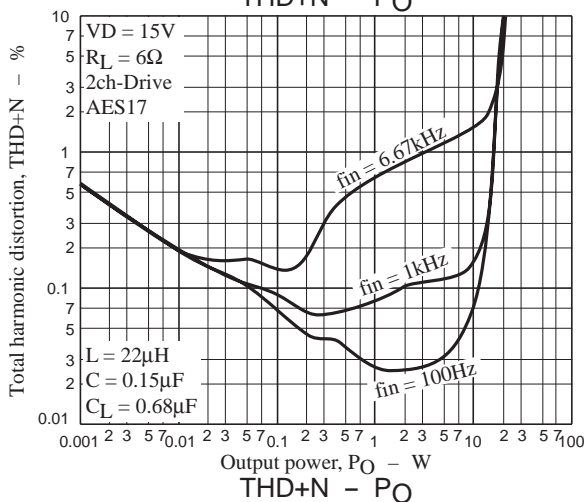
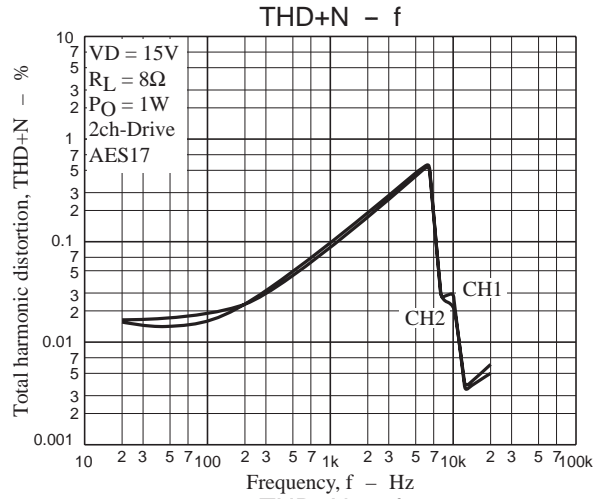
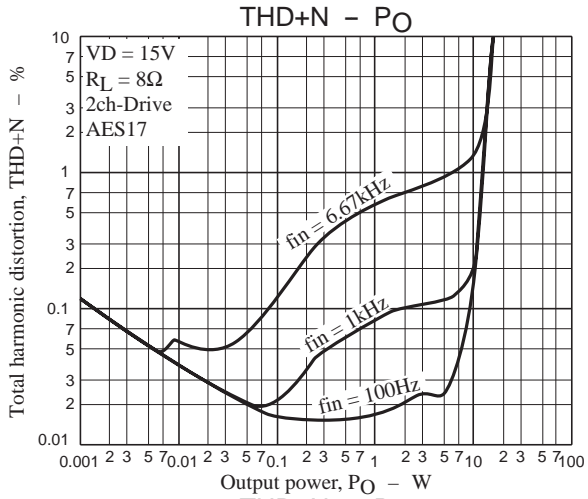
LV49152V

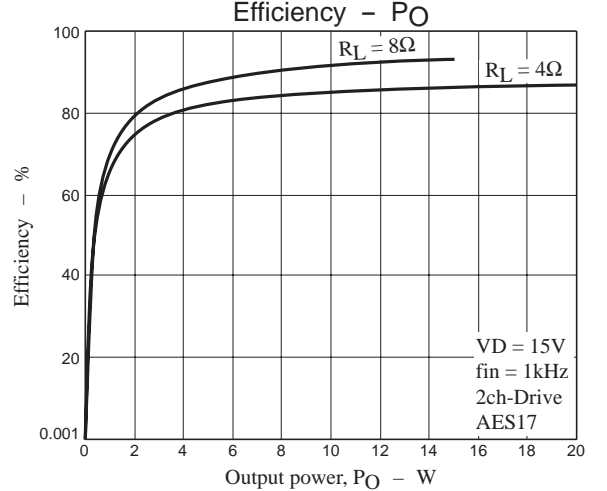
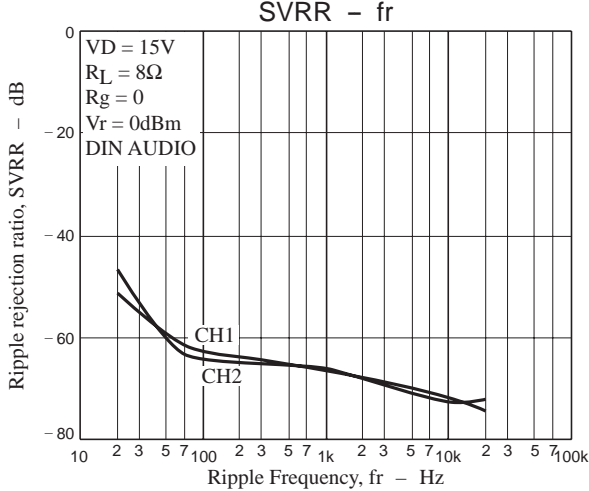
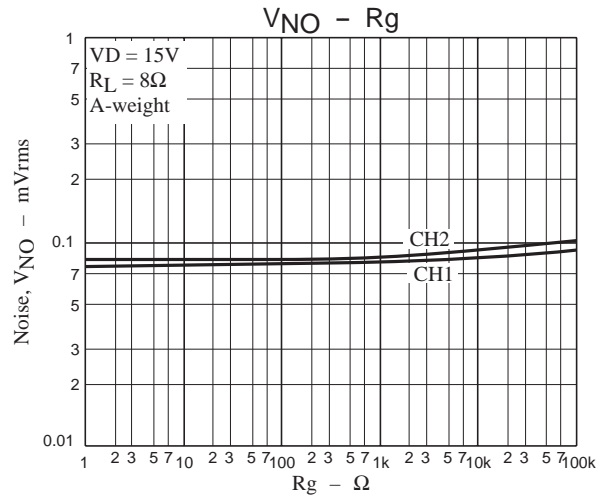
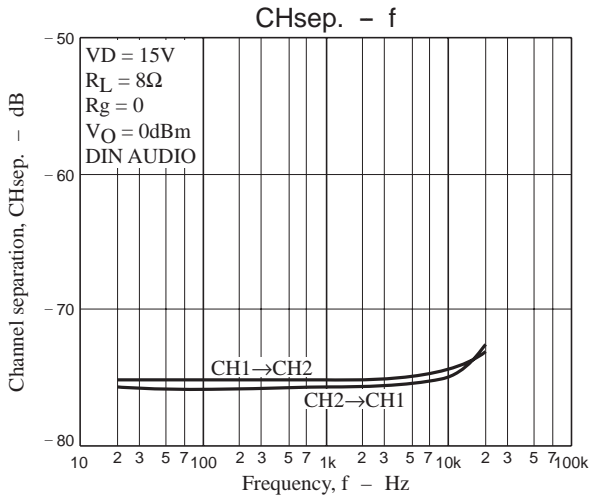


LV49152V



LV49152V





- SANYO Semiconductor Co.,Ltd. assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all SANYO Semiconductor Co.,Ltd. products described or contained herein.
- SANYO Semiconductor Co.,Ltd. strives to supply high-quality high-reliability products, however, any and all semiconductor products fail or malfunction with some probability. It is possible that these probabilistic failures or malfunction could give rise to accidents or events that could endanger human lives, trouble that could give rise to smoke or fire, or accidents that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all SANYO Semiconductor Co.,Ltd. products described or contained herein are controlled under any of applicable local export control laws and regulations, such products may require the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written consent of SANYO Semiconductor Co.,Ltd.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the SANYO Semiconductor Co.,Ltd. product that you intend to use.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production.
- Upon using the technical information or products described herein, neither warranty nor license shall be granted with regard to intellectual property rights or any other rights of SANYO Semiconductor Co.,Ltd. or any third party. SANYO Semiconductor Co.,Ltd. shall not be liable for any claim or suits with regard to a third party's intellectual property rights which has resulted from the use of the technical information and products mentioned above.

This catalog provides information as of April, 2009. Specifications and information herein are subject to change without notice.